#3

INFORMATION DISCLOSURE CITATION PTO-1449 Atty. Docket No. 001155

Serial No. 09/660,439

Applicants:

Toshiyuki TAKEMORI et al.

Filing Date: September 12, 2000

OCT 1 9 2000 15

U.S. PATENT DOCUMENTS

Examiner Initial,	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
A A A A A A A A A A A A A A A A A A A	B C D	Blanchard	8/30/88			EIVED 5 2000 CENTER 2800

FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Translation (Yes or No)
M	AF	8-23092	1/23/96	JAPAN	Abstract
	AG				
	AH				
	AI				
	AJ				

OTHER DOCUMENTS

AK
Daisuke Ueda et al., "An Ultra-Low On-Resistance Power MOSFET Fabricated by Using a Fully Self-Aligned Process" IEEE Transaction on Electron Devices, Vol. ED-34, No. 4, April 1987, pp926-930.

D. Kinzer et al., "Ultra-Low Rdson 12 v P-channel trench MOSFET", The 11th International Symposium in Power Semiconductor Devices and Ics, May 26-28, 1999, pp303-306.

Examiner

Date Considered

Date Considered

. -

NOV 1 3 2001

NOV 1 3 2001

APPLICATE DOCKET No. 001155

Serial No. 09/660,439

Applicants: Toshiyuki TAKEMORI et al.

Filing Date: September 12, 2000

Group Art Unit: 2812

U.S. PATENT DOCUMENTS

INFORMATION DISCLOSURE

CITATION PTO-1449

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
AA AB AC AD AE	5,689,121	Kitagawa et al.	11/18/97	257	139	

FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Translation (Yes or No)
SU	AF	WO 97/33309	09/12/97	PCT	Yes
80-	AG	61-150280	07/08/86	Japan	Abstract
- GV	AH	GB 2 347 014 A	08/23/00	Great Britain	Yes
	AI				
	AJ				

OTHER DOCUMENTS

